

FIG.1A

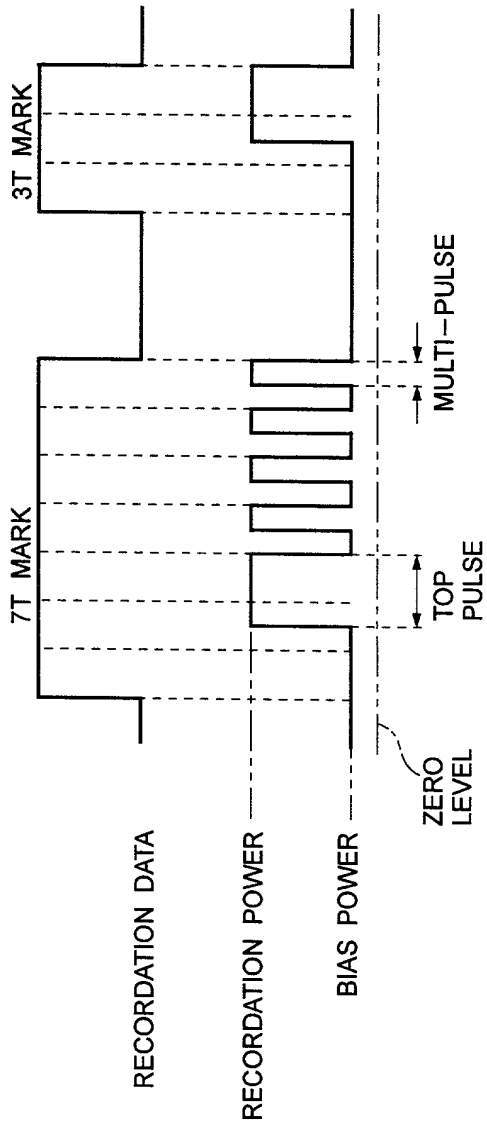


FIG.1B

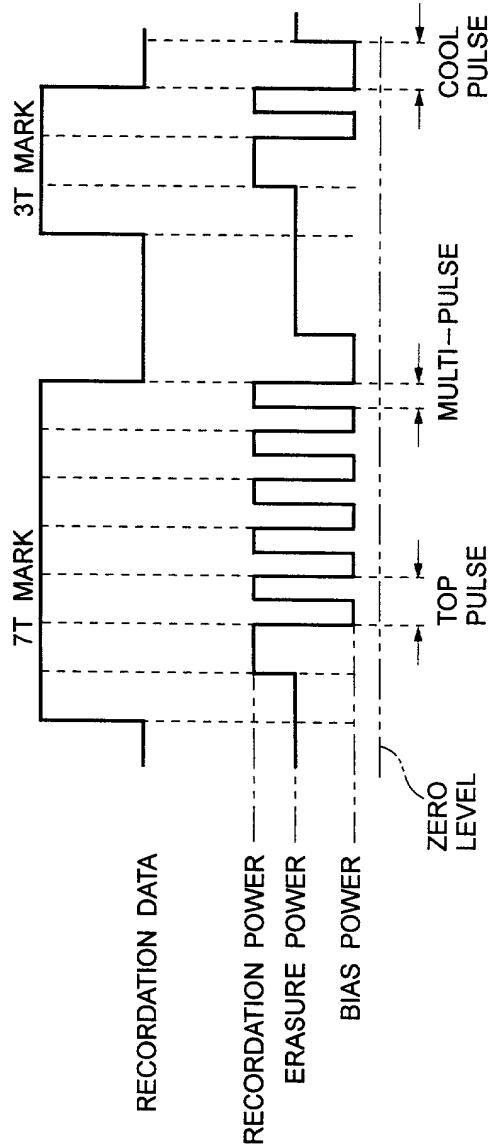


FIG.2A

FIG.2B

FIG.3A

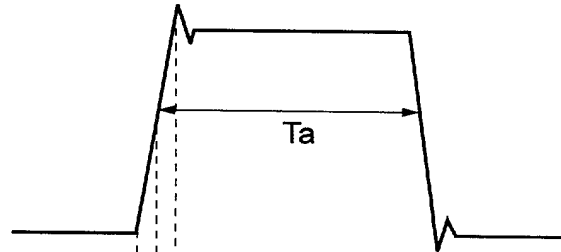


FIG.3B

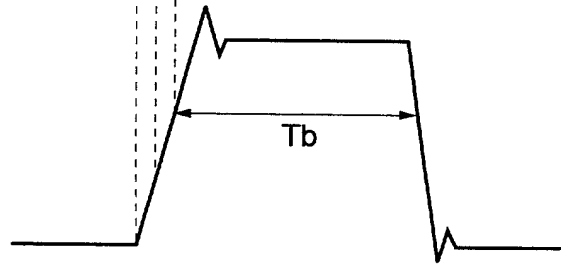


FIG.4A

WRITE PULSE

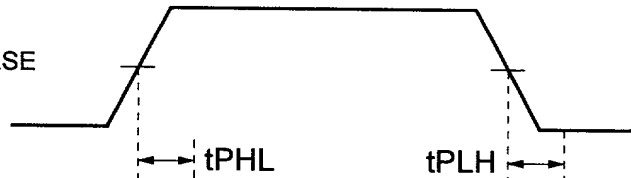


FIG.4B

IN PHASE OUTPUT

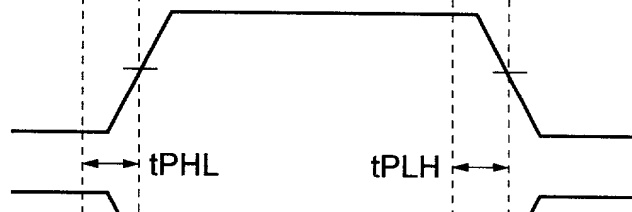
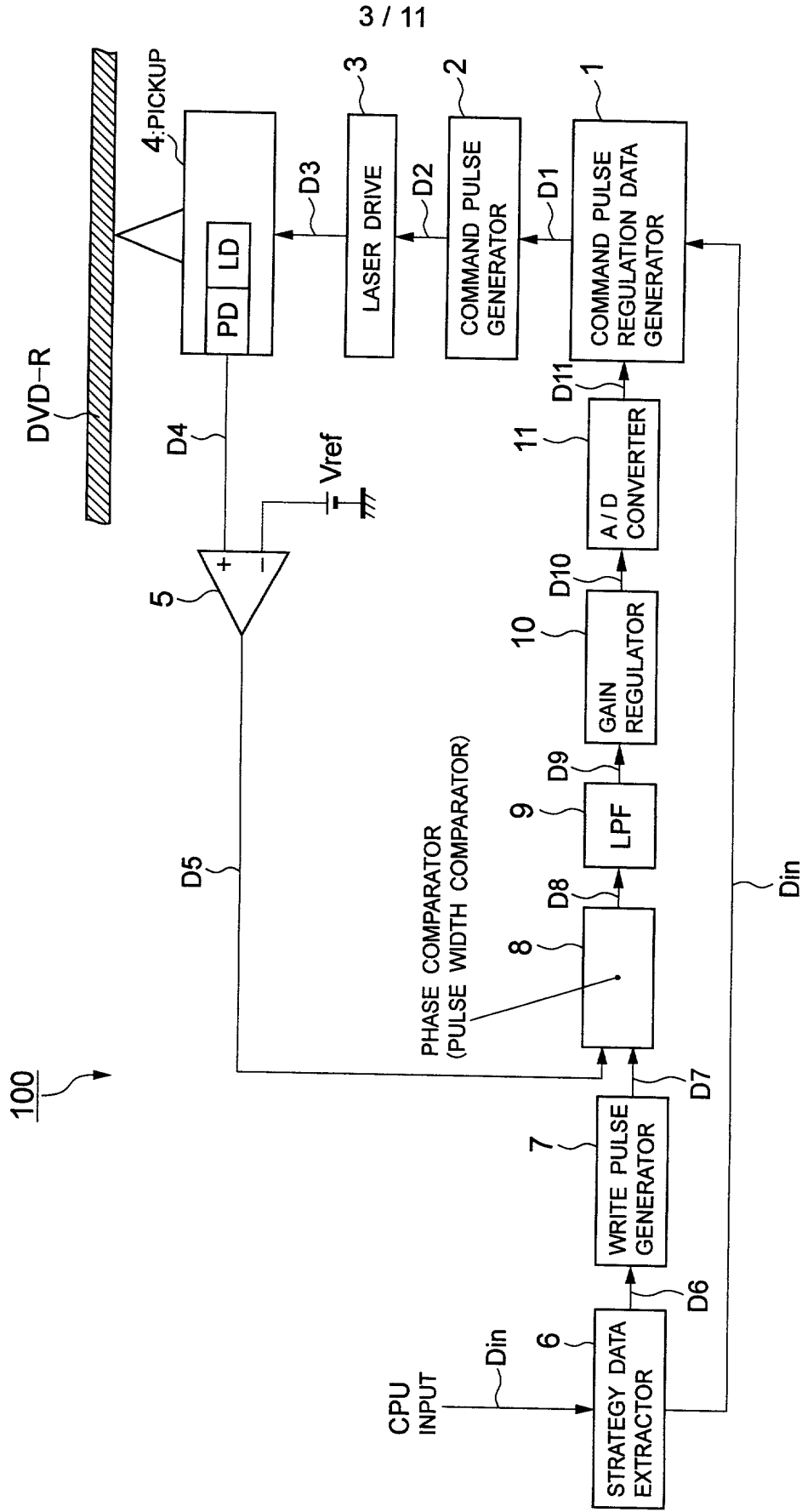


FIG.4C

INVERTED OUTPUT



FIG. 5



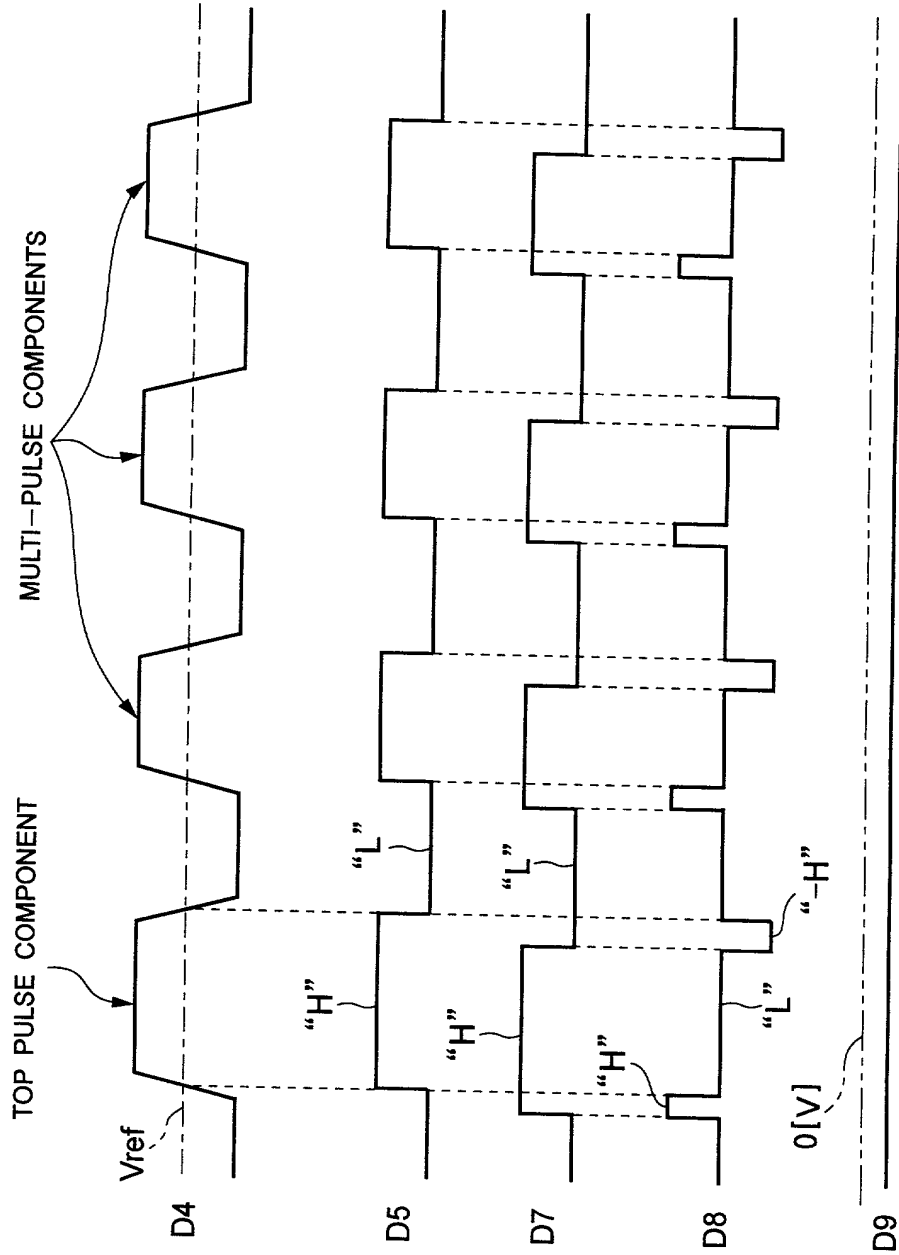


FIG.6A

FIG.6B

FIG.6C

FIG.6D

FIG.6E

TIME

FIG.7A

FIG.7B

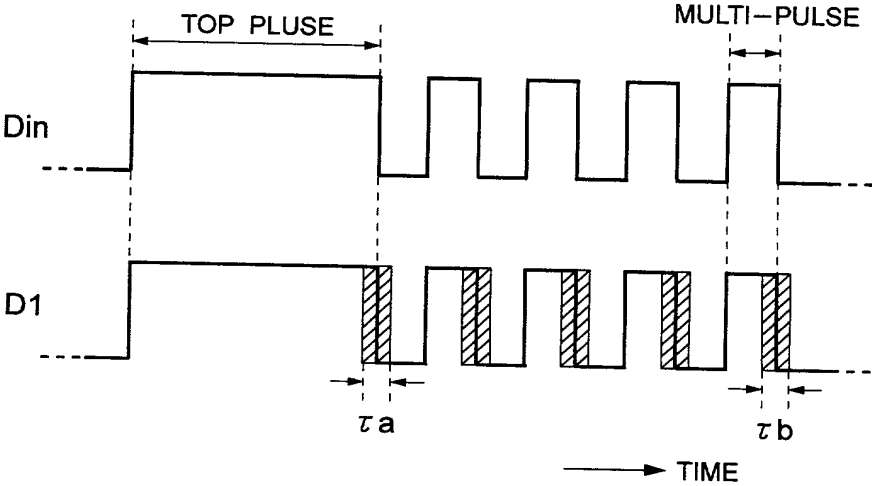


FIG. 8

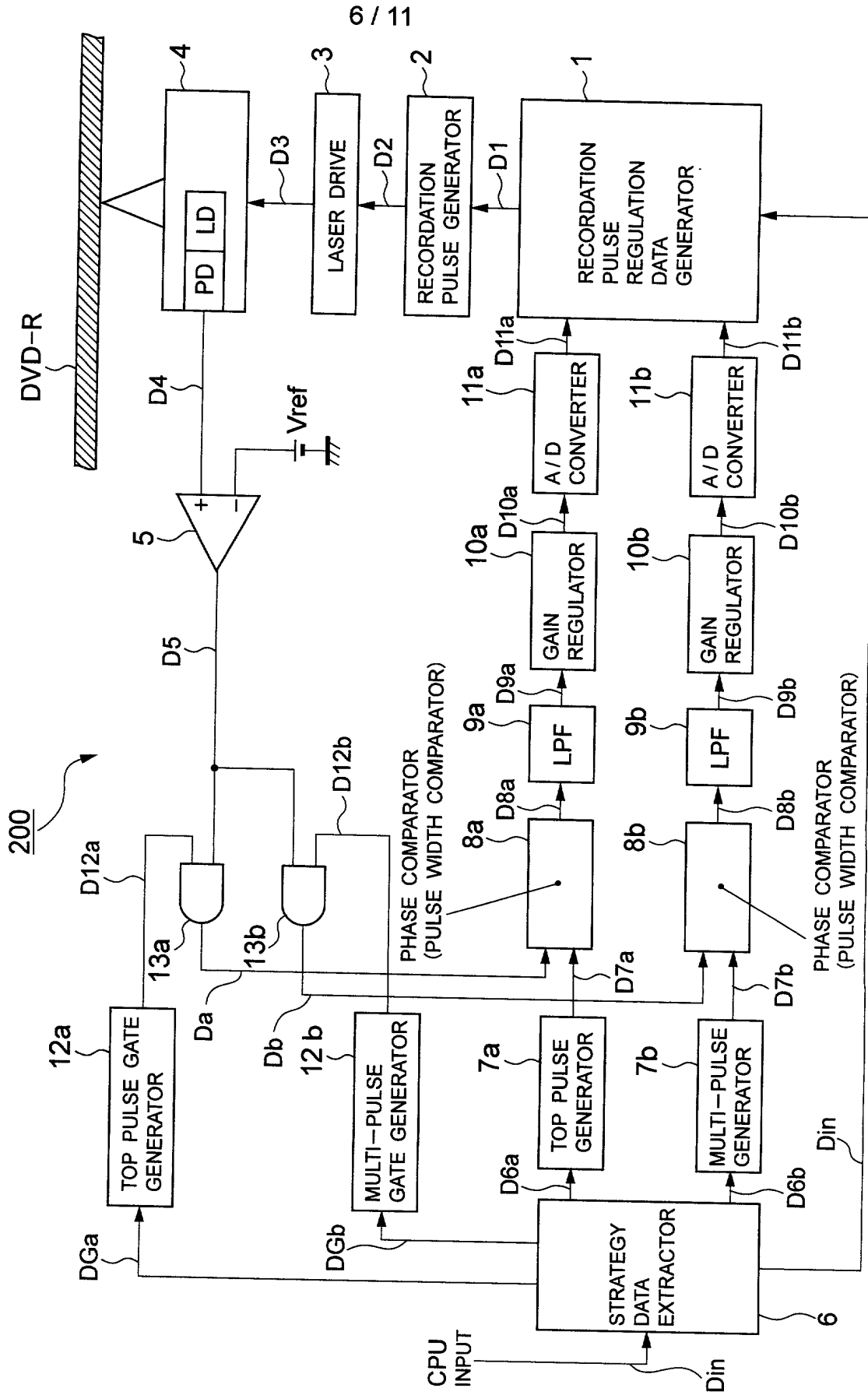


FIG. 9A

FIG. 9B

FIG. 9C

FIG. 9D

FIG. 9E

FIG. 9F

FIG. 9G

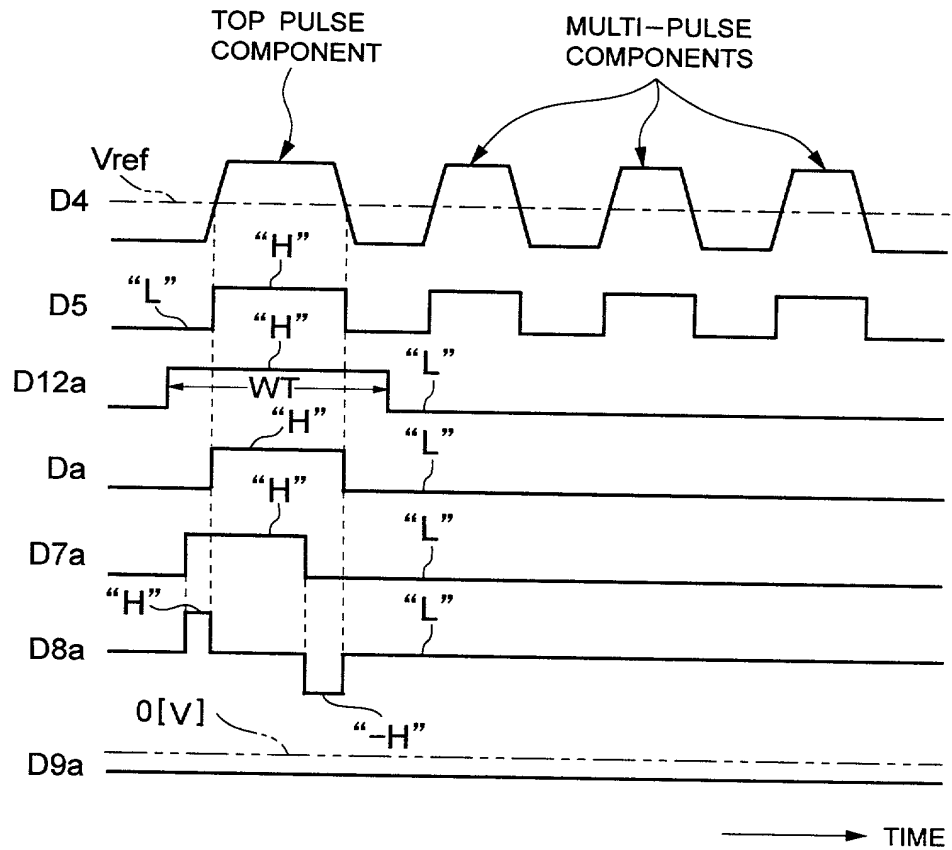


FIG. 10A

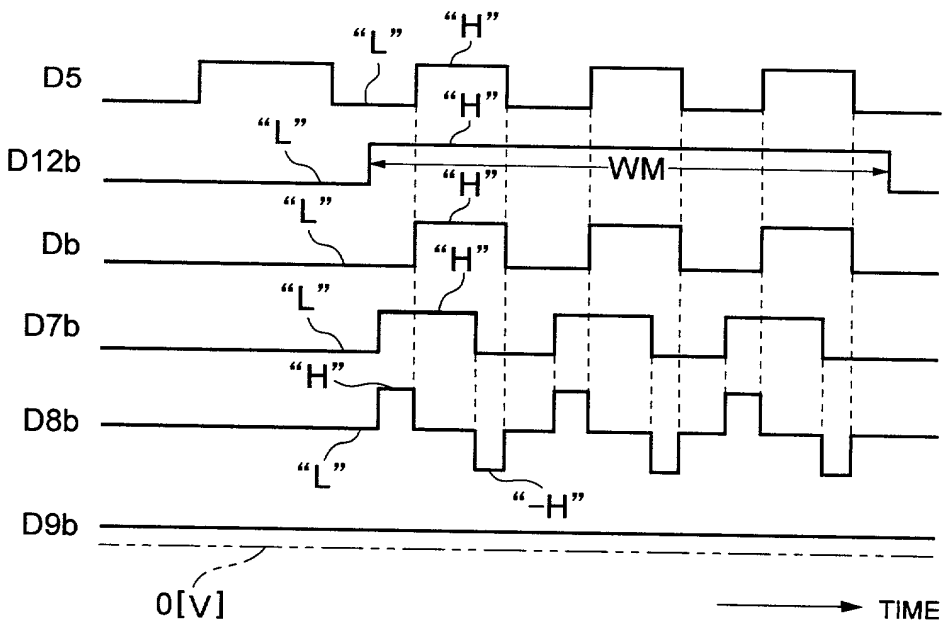
FIG. 10B

FIG. 10C

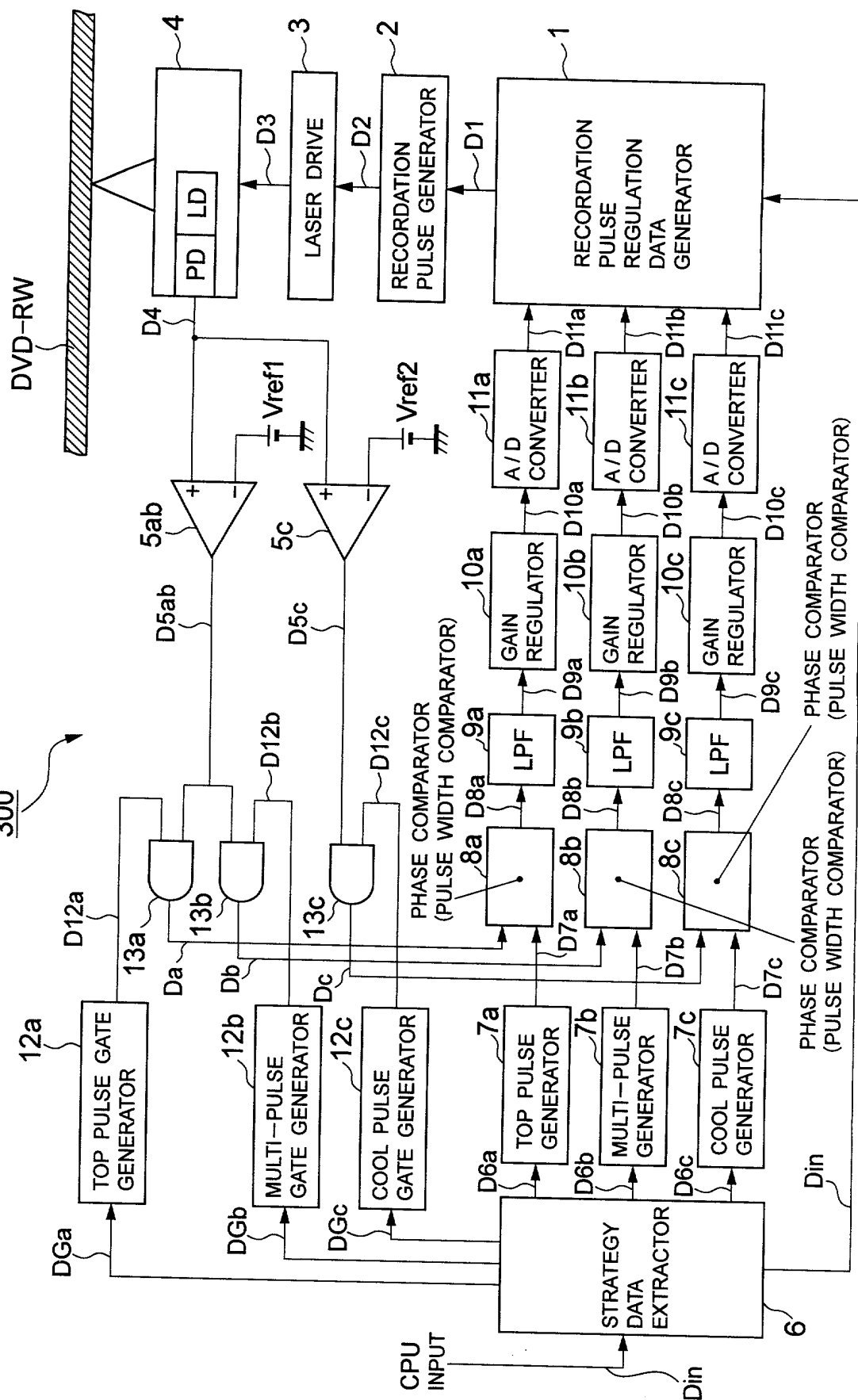
FIG. 10D

FIG. 10E

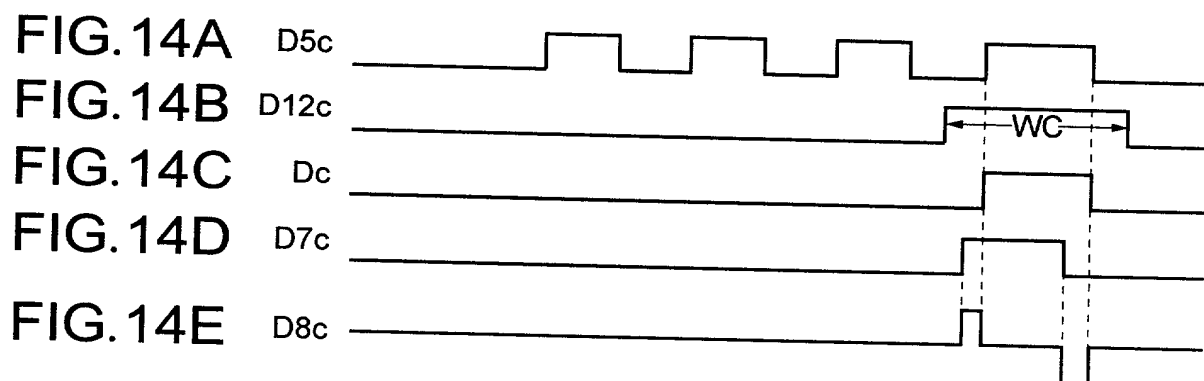
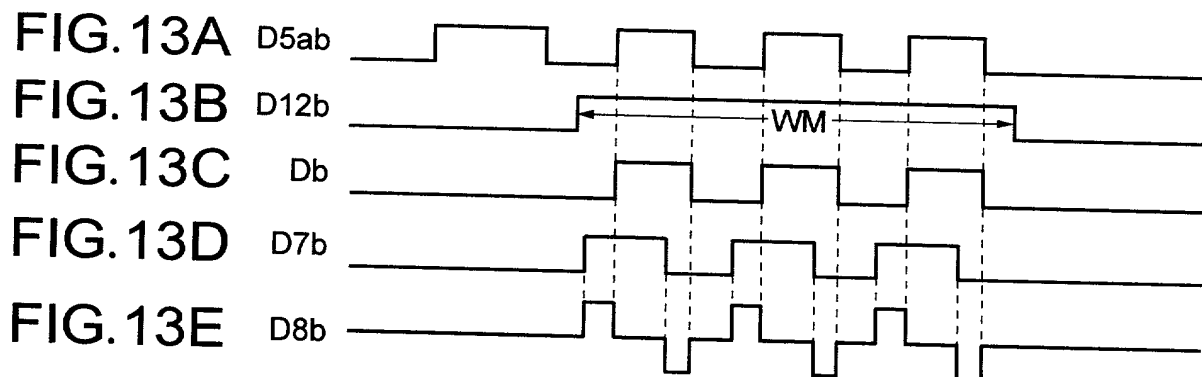
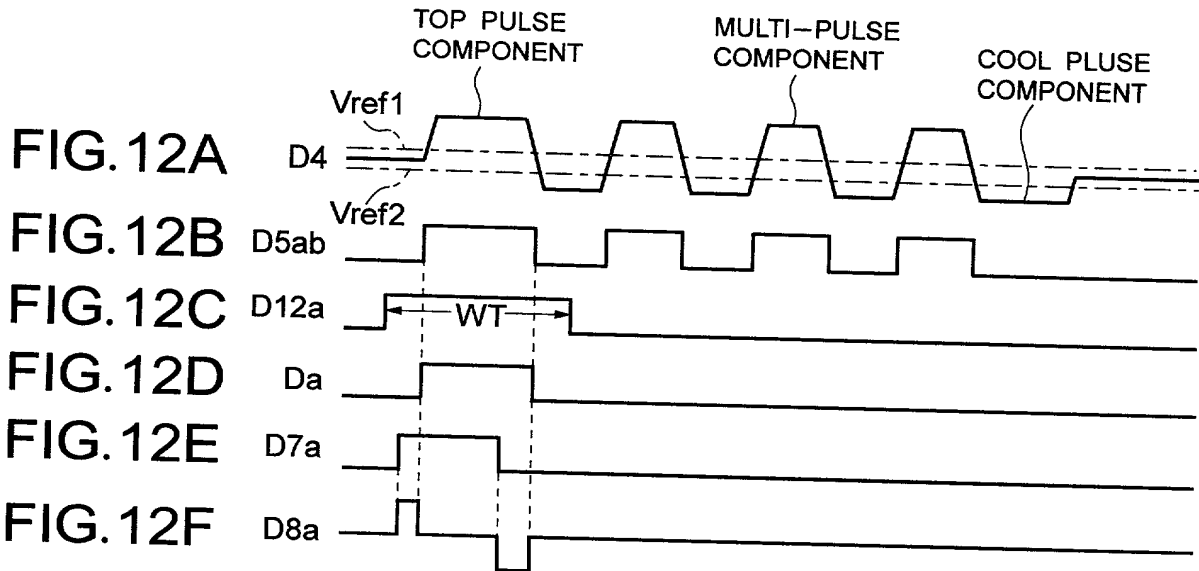
FIG. 10F



300







TIME

FIG. 15A

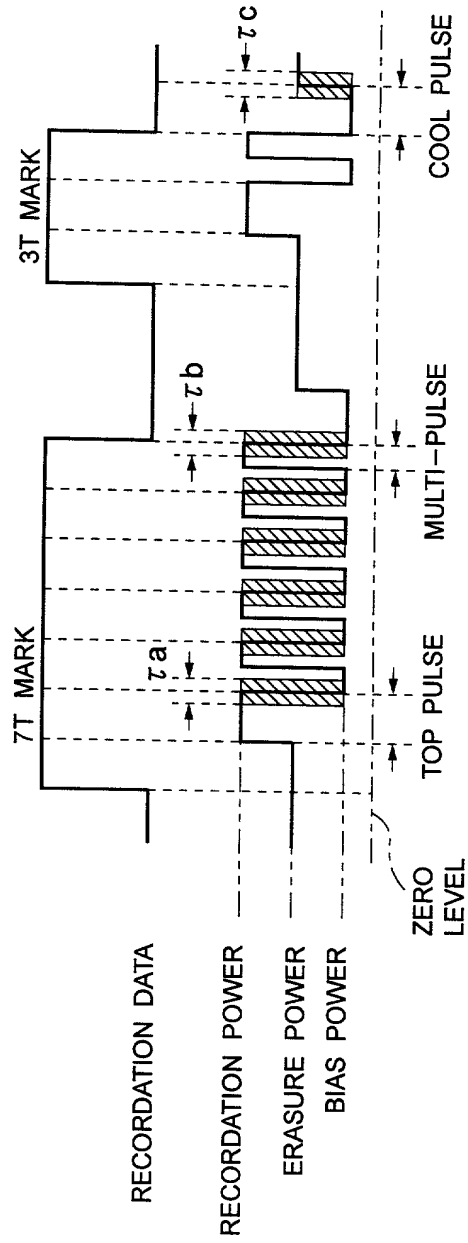


FIG. 15B

FIG. 16

